

	Class	Subclass
ISSUE CLASSIFICATION		

M.H D.I.P.E. 910 SCANNED Q.A. A.G.	PATENT DATE
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/751372		712	218	2183	Chan, Eddie

APPLICANTS

Anthony Jarvis
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Title

System and method for executing variable latency load operations in a data processor

PTO-2040
12/89[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)		ISSUE FEE	
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